Attachment A

L Number	Hits	Search Text	DB	Time stamp
1	997799	(static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor	USPAT; US-PGPUB; EPO; JPO;	2004/03/16 09:26
2	68882	<pre>write near2 (port or circuit or control\$4 or transistor)</pre>	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/03/16 09:27
3	135092	read near2 (port or circuit or control\$4 or transistor)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/03/16 09:27
4	1453	first near (write near2 (port or circuit or control\$4 or transistor))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/03/16
5	1135	<pre>second near (write near2 (port or circuit or control\$4 or transistor))</pre>	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/03/16 09:28
6	2315	<pre>first near (read near2 (port or circuit or control\$4 or transistor))</pre>	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/03/16 09:28
7	1780	second near (read near2 (port or circuit or control\$4 or transistor))	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/03/16
8	38850	memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control\$4 or	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/03/16 09:29
9	25730	memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control\$4 or transistor))) and (read near2 (port or	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29
10	796	memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control\$4 or transistor))) and (read near2 (port or circuit or control\$4 or transistor))) and (first near (write near2 (port or circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29
11	347	or control\$4 or transistor))) (((((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control\$4 or transistor))) and (read near2 (port or circuit or control\$4 or transistor))) and (first near (write near2 (port or circuit or control\$4 or transistor)))) and (second near (write near2 (port or circuit or control\$4 or transistor))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29

12	171	((((((static near2 ram) or (static near2	USPAT;	2004/03/16
		memory) or sram or latch or register or	US-PGPUB;	09:29
		inverter or invertor) and (write near2	EPO; JPO;	
		(port or circuit or control\$4 or	DERWENT;	†
		transistor))) and (read near2 (port or	IBM TDB	
		circuit or control\$4 or transistor))) and	_]
		(first near (write near2 (port or circuit		1
		or control\$4 or transistor)))) and		
		(second near (write near2 (port or		ŧ
		circuit or control\$4 or transistor))))		
		and (first near (read near2 (port or		
		circuit or control\$4 or transistor)))		
13	138	(((((((static near2 ram) or (static near2	USPAT;	2004/03/16
		memory) or sram or latch or register or	US-PGPUB;	09:30
		inverter or invertor) and (write near2	EPO; JPO;	
		(port or circuit or control\$4 or	DERWENT;	
		transistor))) and (read near2 (port or	IBM_TDB	
	İ	circuit or control\$4 or transistor))) and	_	
		(first near (write near2 (port or circuit	ļ	
		or control\$4 or transistor)))) and		
		(second near (write near2 (port or		
		circuit or control\$4 or transistor))))		
		and (first near (read near2 (port or		
		circuit or control\$4 or transistor))))		1
		and (second near (read near2 (port or		
		circuit or control\$4 or transistor)))		
14	28	(((((((static near2 ram) or (static	USPAT;	2004/03/16
		near2 memory) or sram or latch or	US-PGPUB;	09:31
		register or inverter or invertor) and	EPO; JPO;	-
		(write near2 (port or circuit or	DERWENT;	
		control\$4 or transistor))) and (read	IBM_TDB	
		near2 (port or circuit or control\$4 or		
		transistor))) and (first near (write		
		near2 (port or circuit or control\$4 or		
		transistor)))) and (second near (write		
		near2 (port or circuit or control\$4 or		
		transistor)))) and (first near (read		
		near2 (port or circuit or control\$4 or		
		transistor)))) and (second near (read		
		near2 (port or circuit or control\$4 or		
		transistor)))) and "nmos transistor"		1